

VLSI Synthesis Of DSP Kernels: Algorithmic And Architectural Transformations By Mahesh Mehendale

By Mahesh Mehendale

Summary Manesh Mehendale, Sunil D. Sherlekar show all 2 hide VLSI Synthesis of DSP Kernels Book Subtitle Algorithmic and Architectural Transformations

Mahesh Mehendale , Sunil D. Sherlekar, VLSI Synthesis of DSP Kernels: Algorithmic and Architectural IEEE Transactions on Very Large Scale Integration

{Word-length optimization for differentiable nonlinear systems}, journal = {ACM Trans. Des. Autom. VLSI Synthesis of DSP Kernels - MEHENDALE,

The principles of the design of a digital signal processing system using VLSI technologies. 2. VLSI Synthesis of DSP kernels. 10.

Mahesh Mehendale, Sunil D. Sherlekar VLSI Synthesis of DSP Kernels: Algorithmic and Architectural Transformations Language: English Pages: 209 Publisher: Springer

View Mahesh Mehendale's business profile as He has co-authored a book on "VLSI synthesis of DSP kernels" published by Kluwer Academic Publishers and a chapter

Digital signal processing (DSP) The algorithms required for DSP are sometimes performed using specialized computers,

VLSI Synthesis of DSP Kernels : Algorithmic and Architectural Transformations. [Manesh Mehendale; VLSI Synthesis of DSP Kernels presents the following:

SoC - The Road Ahead where he focuses on the methodologies and design of SoCs targeted to multiple DSP VLSI synthesis of DSP kernels published by Kluwer.

The main contribution of this study is the development of an area-/energy-efficient cascaded direct-truncation (DT) VLSI Synthesis of DSP Kernels

Keynote Speaker: Dr. Mahesh Mehendale. He has co-authored a book on VLSI synthesis of DSP kernels published by Kluwer Academic Publishers and a chapter on

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Mahesh Mehendale: High Level Synthesis Of Multi Implementation of DSP Algorithms. VLSI Algorithmic and Architectural Transformations for Low

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